

## URSI Commission C in Japan

21th URSI-C-Japan Chairperson Kenji ITOH

## 1. Domestic Activities

## 1.1 The 10th scientific workshop presented by the 21th URSI-C in Japan

i) Convener: Dr. Tatsuya Ohguro, Toshiba Corp.

ii) Date/time: Workshop 13:00 - 17:00, Night session 21:00-22:00 February 4, 2011.

iii) Venue: The 2nd floor, Mori no Yado, Miyajima, Hiroshima, Japan

iv) Registration fee: Free

v) Listed attendees: 29 persons

vi) Local arrangement: Prof. Fujishima (Hiroshima University)

vii) Session title: "Limitation of integrated circuit"

viii) Presentation:

1. " Future vision of scaled Si CMOS device"

Prof. Hiramoto (Univ. of Tokyo)

2. "Pass, now and future of device and circuit technology for Si RF SoC"

Prof. Matsuzawa (Tokyo Institute of Technology)

3. "Technology strategy of platform in analog semiconductor - position of analog semiconductor in business"

Prof. Hamasaki (Hiroshima Institute of Technology)

4. "Trend of RF circuit for advanced CMOS"

Dr. Tanaka (Renesas technology)

ix) Reception and Lamp Session:

"Tera-hertz CMOS circuit" , Dr. Fujimoto (STARC)

x) The steering committee meeting took place from 08:00 to 9:00 on 5-Feb.-2011.

## 2. International Activities

i) URSI-GA 2011 activities

URSI-C members described the URSI national report for URSI-GA 2011.